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PATENT APPLICATION 10/825,345

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shannon V. Davidson et al.

Serial No.:

10/825,345

Filing Date:

April 15, 2004

Title:

System and Method for Computer Cluster Virtualization Using

Dynamic Boot Images and Virtual

Mail Stop: Amendments
Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

Dear Sir:

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I hereby certify that this communication is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" under 37 C.F.R. § 1.10 on the date indicated below and is addressed to The Commissioner For Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Willie Jiles

Date: March 8, 2006

INFORMATION DISCLOSURE STATEMENT

Applicants respectfully request, under 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. The present Application was filed after June 30, 2003. Therefore, under the July 11, 2003, waiver of 37 C.F.R. § 1.98(a)(2)(i) by the U.S. Patent and Trademark Office, copies of U.S. Patents and U.S. Patent Application Publications listed in the attached PTO-1449 form are not enclosed. All other references are enclosed for the convenience of the Examiner.

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REMARKS

Each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than thirty days prior to the filing of this Information Disclosure Statement, and this Information Disclosure Statement is submitted before a first Office Action on the merits. Under 37 C.F.R. § 1.97(b)(3), no fee is due. Nonetheless, the Commissioner is hereby authorized to charge any fee and credit any overpayment to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,

BAKER BOTTS L.L.P. Attorneys for Applicants

Travis W. Thomas Reg. No. 48,667

Date: March 8, 2006

Correspondence Address:

Customer Number 45507

Application No. Applicant(s) PTO-1449 10/825.345 Shannon V. Davidson et al. Information Disclosure Citation Docket Number **Group Art Unit Filing Date** In an Application April 15, 2004 064747.1011 U.S. PATENT DOCUMENTS **SUBCLASS** FILING DATE OCUMENT NO. DATE NAME **CLASS** 08/07/1990 04/05/1994 Yalamanchili 395 800 5,301,104 /s.b.**^** B FOREIGN PATENT DOCUMENTS TRANSLATION **SUBCLASS** DOCUMENT NO. DATE **COUNTRY CLASS** YES NO C D NON-PATENT DOCUMENTS DATE **DOCUMENT (Including Author, Title, Source, and Pertinent Pages)** Communication from the European Patent Office, European Search Report for Application No. Mailed \mathbf{E} PCT/US2005/012314, 6 pages, 09/20/2005, and Written Opinion of the International Search 02/10/2006 /S.D./ Authority, 7 pages Feitelson, "Job Scheduling in Multiprogrammed Parallel Systems", IBM Research Report, August August 1997 /S.D./ 1997, pages 1-4, XP002942107 1998 Kandlur et al., "Hypercube Management in the Presence of Node Failures", third conference on /Sl.DG/ hypercube concurrent computers and applications ACM New York, 1988, pages 328-336, vol. 1, XP002364194 Wenjian et al., "Efficient Processor Allocation for 3D Tori", Parallel Processing Symposium 1995 /SLDH/ 1995, Proceedings, 9th International, April 25-28, 1995, IEEE Comput. Soc., pages 466-471, XP010135921 Ma et al., "E-Kernel: An Embedding Kernel on the IBM Victor V256, Multiprocessor for /SI.Di/ September 1, 1994 Program Mapping and Network Reconfiguration, IEEE Transactions on Parallel and Distributed Systems, IEEE Service Center, vol. 5, no. 9, September 1, 1994, pages 977-994, XP000460490 DATE CONSIDERED **EXAMINER** /Saket Daftuar/ (06/10/2008) EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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